

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Joseph Huang et al.

Filed: 1/15/2004

For: Apparatus and Methods for Providing Redundancy in Integrated Circuits

Serial No.: 10/757,928

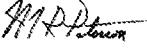
Group Art Unit: 2827

Examiner: Weinberg, Michael J.

Attorney Dkt: ALTR:023

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

Date: 6/1/2007

Name: 

REQUEST FOR EXTENSION OF TIME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.136(a), Applicants request a 2-month extension of time in which to file the enclosed response or paper in the above-identified application. The Applicant encloses with this paper the \$450.00 fee for the extension.

If any fees (other than the fee for this extension of time, which the Applicant encloses with this paper) are due under 37 C.F.R. §§ 1.16-.21 for any reason relating to the enclosed materials, the Commissioner is authorized to deduct such fees from Deposit Account No. 50-3813/ALTR023.

Respectfully submitted,



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